

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Takeda et al.)
 Serial No. 10/791,163)
 Conf. No. 3403)
 Filed: March 2, 2004)
 For: LIQUID CRYSTAL DISPLAY)
 DEVICE AND THIN FILM)
 TRANSISTOR SUBSTRATE)
 Art Unit: 2871)
 Examiner: Not assigned)

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

November 23, 2004

Date

F-CLASS.WCM

Appr. February 20, 1998 Attorney for Applicant

[Signature]
 Registration No. 29,367

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby request that a corrected Filing Receipt be issued which indicates the correct spelling of a named inventor. The Patent and Trademark Office inadvertently listed an inventor's name as "HIDEFUMI SHIDA." The correct spelling of the inventor's name should be -HIDEFUMI YOSHIDA-.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

Customer No. 24978

November 23, 2004

300 South Wacker Drive
 Suite 2500
 Chicago, Illinois 60606
 Telephone: (312) 360-0080
 Facsimile: (312) 360-9315

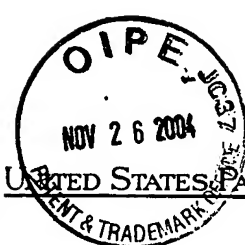
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By

[Signature]

Patrick G. Burns

Registration No. 29,367



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/791,163	03/02/2004	2871	1218	1508.69867	54	21	8

CONFIRMATION NO. 3403

CORRECTED FILING RECEIPT



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Patrick G. Burns, Esq.
 GREER, BURNS & CRAIN, LTD.
 Suite 2500
 300 South Wacker Drive
 Chicago, IL 60606

Date Mailed: 10/29/2004

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Applicant(s)

Arihiro Takeda, Kawasaki, JAPAN;
 Yoshio Koike, Kawasaki, JAPAN;
 Takahiro Sasaki, Kawasaki, JAPAN;
 Kouji Tsukao, Yonago, JAPAN;
 Shingo Kataoka, Kawasaki, JAPAN;
 Kimiaki Nakamura, Kawasaki, JAPAN;
 Yuichi Inoue, Kawasaki, JAPAN;
 Kazutaka Hanaoka, Kawasaki, JAPAN;
 Seiji Tanuma, Kawasaki, JAPAN;
 Takatoshi Mayama, Kawasaki, JAPAN;
~~Hidefumi Shida, Kawasaki, JAPAN;~~
 Yasutoshi Tasaka, Kawasaki, JAPAN;
 Takashi Sasabayashi, Kawasaki, JAPAN;
 Yohei Nakanishi, Kawasaki, JAPAN;

Hidefumi Yoshida

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Assignment For Published Patent Application

FUJITSU DISPLAY TECHNOLOGIES CORPORATION, Kawasaki-shi, JAPAN

Power of Attorney:

Roger Greer-26174
 Patrick Burns-29367
 Lawrence J. Crain-31497
 Steven Fallon-35132
 James Folker-37538

Jonathan Feuchtwang-41017
 B. Joe Kim-41895
 Joel Bootzin-42343

Domestic Priority data as claimed by applicant

This application is a DIV of 09/662,236 09/14/2000

Foreign Applications

JAPAN 11-262798 09/16/1999

JAPAN 11-374720 12/28/1999

JAPAN 2000-194170 06/18/2000

If Required, Foreign Filing License Granted: 05/22/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/791,163

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Liquid crystal display device and thin film transistor substrate

Preliminary Class

349

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